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Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

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Substitute for Form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)				Complete If Known	
				Application Number	10/1674771
				Filing Date	9/30/03
				First Named Inventor	Visokay et al.
				Group Art Unit	2812
Examiner Name	Kennedy				
Sheet	1	of	2	Attorney Docket No.	TI-35943

U.S. PATENT DOCUMENTS						
Exam. Initials*	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Doc.	Date of Pub. of Cited Doc. (mm-dd-yyyy)	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code ² (if known)			
gll	AA	4,628,588		McDavid	12/16/1986	Entire Document
gll	AB	4,641,417		McDavid	02/10/1987	Entire Document
gll	AC	4,672,419		McDavid	06/09/1987	Entire Document
gll	AD	4,954,867		Hosaka	09/04/1990	Entire Document
gll	AE	5,633,522		Dorleans et al.	05/27/1997	Entire Document
gll	AF	5,723,893		Yu et al.	03/03/1998	Entire Document
gll	AG	5,937,315		Xiang et al.	08/10/1999	Entire Document
gll	AH	6,376,342	B1	Tseng	04/23/2002	Entire Document
gll	AI	6,410,967	B1	Hause et al.	06/25/2002	Entire Document
gll	AJ	6,432,817	B1	Bertrand et al.	08/13/2002	Entire Document
gll	AK	6,602,781	B1	Xiang et al.	08/05/2003	Entire Document
gll	AL	6,617,624	B2	Powell	09/09/2003	Entire Document

FOREIGN PATENT DOCUMENTS								
Exam. Initials*	Cite No. ¹	Foreign Patent Document			Name of Patentee or Applicant of Cited Doc.	Date of Pub. of Cited Doc. (mm-dd-yyyy)	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Office ³	Number ⁴	Kind Code ² (if known)				
	BA							
	BB							
	BF							
	BG							
	BH							

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS				
Exam. Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.		T ²
gm	CA	"Mo ₂ N/Mo GAPP MOSFETs", MANJIN J. KIM and DALE M. BROWN, IEEE, 1982, pp. 560-563.		
gm	CB	"Application of MoSi ₂ to the Double-Level Interconnections of I ² L Circuits", YOSHITAKA SASAKI, OSAMU OZAWA and SHUICHI KAMEYAMA, IEEE Transactions on Electron Devices, Vol. ED-27, No. 8, August, 1989, 5 pgs.		
gm	CC	"Work Function Controlled Silicide Technology for Submicron CMOS", MASAKAZU KAKUMU and KAZUHIKO HASHIMOTO. Source and date unknown, 2 pgs.		
gm	CD	"REFRACTORY METAL SILICIDE/N+ POLYSILICON IN CMOS/SOS", B. C. LEUNG and J. S. MAA, Source and date unknown, 4 pgs.		

Examiner Signature	<i>James M. Kennedy</i>	Date Considered	<i>July 24, 2004</i>
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US and Foreign Patent Documents: ¹Unique citation designation number. ²See attached Kinds of U.S. Patent Documents. ³Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶Applicant is to place a check mark here if English language Translation is attached.

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Substitute for Form 1449A/PTO				Complete If Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)				Application Number	101674711
				Filing Date	9/30/03
				First Named Inventor	Visokay et al.
				Group Art Unit	2812
				Examiner Name	Kennedy
Sheet	2	of	2	Attorney Docket No.	TI-35943

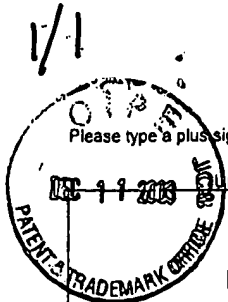
OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS			
Exam. Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
me	CE	"Fabrication of Mo-Gate/Ti-Silicide-Clad-Moat MOS Devices by Use of Multilayer-Glass Depositions", J. M. MCDAVID, IEEE Electron Device Letters, Vol. EDL-5, No. 9, September, 1984, 2 pgs.	
me	CF	"Lightly Impurity Doped (LD) Mo Silicide Gate Technology", MASAKAZU KAKUMU and JUN'ICHI MATSUNAGA, IEEE, 1985, pp. 415-418.	
me	CG	"An Investigation of Molybdenum Gate for Submicrometer CMOS", ROBERT F. KWASNICK, EDMUND B. KAMINSKY, PAUL A FRANK, GERHARD A FRANZ, KENNETH J. POLASKO, RICHARD J. SAIA and THOMAS B. GORCZYA, IEEE Transactions on Electron Devices, Vol. 35, No. 9, September, 1988, 7 pgs.	
me	CH	"Electrical Characteristics of TiB ₂ for ULSI Applications", CHANG SIK CHOI, QINGFENG WANG, CARLTON M. OSBURN, GARY A RUGGLES and AYAN S. SHAH, IEEE Transactions on Electron Devices, Vol. 39, No. 10, October, 1992, pp. 2341-2345.	
me	CI	"Fin FET Process Refinements for Improved Mobility and Gate Work Function Engineering", YANG-KYU CHOI, LELAND CHANG, PUSHKAR RANADE, JEONG-SOO LEE, DAEWON HA, SRIRAM BALASUBRAMANIAN, ADITYA AGARWAL, MIKE AMEEN, TSU-JAE KING, and JEFFREY BOKOR, IEEE, 2002, 4 pgs.	
me	CJ	"Silicon Processing for The VLSI Era, Volume 2: Process Integration", MOS DEVICES AND NMOS PROCESS INTEGRATION, STANLEY WOLF PhD, 1990, 7 PGS.	
	CK		
	CL		
	CM		
	CN		

Examiner Signature	James M. Kennedy	Date Considered	July 24, 2004
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INFORMATION DISCLOSURE
STATEMENT BY APPLICANT

(use as many sheets as necessary)

Complete If Known

Application Number	10/674,771
Filing Date	September 30, 2003
First Named Inventor	Visokay et al.
Group Art Unit	2812
Examiner Name	Kennedy
Attorney Docket No.	TI-35943

Sheet 1 of 1

U.S. PATENT DOCUMENTS

Exam. Initials*	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Doc.	Date of Pub. of Cited Doc. (mm-dd-yyyy)	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code ² (if known)			
MK	AA	2003/0109121	A1	Rotondaro	06/12/2003	Entire Document
	AB					
	AC					
	AD					
	AF					
	AG					

FOREIGN PATENT DOCUMENTS

Exam. Initials*	Cite No. ¹	Foreign Patent Document			Name of Patentee or Applicant of Cited Doc.	Date of Pub. of Cited Doc. (mm-dd-yyyy)	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ³
		Office ³	Number ⁴	Kind Code ² (if known)				
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	BB							
	BF							
	BG							

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MK	CA	"High Speed CMOS Structure with Optimized Gate Work Function", T. NOGUCHI, Y. ASAHU, M. NAKAHARA, K. MAEGUCHI and K. KANZAKI. Source and Date not Available. 2 pgs.	
	CB		
	CC		
	CD		
	CE		
	CF		
	CG		
	CH		

Examiner Signature	<i>Matthew M. Kennedy</i>	Date Considered	<i>July 21, 2004</i>
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